

# **Advanced ASIC Chip Synthesis: Using Synopsys® Design Compiler(TM) And PrimeTime® By Himanshu Bhatnagar**

**By Himanshu Bhatnagar**

Advanced ASIC Chip Synthesis: Using Synopsys' Design Compiler and PrimeTime

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Himanshu Bhatnagar is the author of Advanced ASIC Chip Synthesis (3.00 avg rating, 2 ratings, 0 reviews, published 1999), Himanshu Bhatnagar's Followers.

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