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By Chris J. Myers

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by Chris J. Myers, Wendy Belluomini, Kip Killpack, Eric Mercer, Eric Peskin, Hao Zheng - in Proc. of Asia and South Pacific Design

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U of U Biomedical Engineering - University of Utah -

Chris J. Myers. Adjunct Professor, Electrical Engineering, Stanford University, and asynchronous circuit design.

<http://www.bioen.utah.edu/directory/profile.php?userID=66>

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http://www.inf.uth.gr/?page_id=15103&lang=en

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My current research interests are asynchronous circuit design, Biography Chris J. Myers received the B.S. degree in electrical engineering and Chinese

https://faculty.utah.edu/u0030884-CHRIS_J_MYERS/biography/index.html

Faculty in ECE - University of Utah -

Chris J. Myers. Professor Office: Email: myers@ece.utah.edu Area: Asynchronous circuit design
Prof. Myers' Profile The University of Utah;
<http://www.ece.utah.edu/faculty>

Course Title: Digital Design Using VHDL -

Asynchronous Circuit Design, Chris J. Myers, Prentice Hall 2001. Grading policy: The ability to analyze and synthesis fundamental asynchronous circuit design.
http://www.engr.sjsu.edu/caohuut/EE270/Documents/ee270_syl_F09.docx

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design Chris Myers Wendy Belluomini Kip Killpack Eric Mercer C. J. Myers. Asynchronous Circuit Design. Wiley, New York, 2001. C. J. Myers, T. G. Rokicki,
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<http://infolab.stanford.edu/pub/ctr/reports/csl/tr/94/648/CSL-TR-94-648.pdf>

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My current research interests are asynchronous circuit design, and design of genetic circuits. Faculty Profile > CHRIS J MYERS CHRIS J
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