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By Richard F. Tinder

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that counteract robotic adversarial interventions in the operation of asynchronous sequential machines.

Asynchronous sequential machine design and

<http://www.tandfonline.com/doi/full/10.1080/00207179.2015.1064547>

Sequential logic is used to construct finite state machines, Asynchronous sequential logic is not synchronized by a clock Logic design; Asynchronous logic

http://en.wikipedia.org/wiki/Sequential_logic

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A design procedure is presented which allows for detection of faults in asynchronous sequential machines in a real time environment. Faults affecting both the output

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several versions of the XAP processor experimented with different asynchronous design styles: Sequential logic (asynchronous) References Finite-state machine;

http://en.wikipedia.org/wiki/Asynchronous_circuit

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forming logic in synchronous sequential machines. Asynchronous sequential pass transistor partitioning for the machine design of asynchronous circuits:

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The traditional methods of design for asynchronous sequential machines involve a careful study of the flow table to identify and deal with the critical races and hazards.

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The standard methods for design of asynchronous sequential machines are usually complicated due to critical races and hazards, and necessitate special state

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