

# Creating Assertion-Based IP (Integrated Circuits And Systems) By Harry D. Foster;Adam C. Krolnik

By Harry D. Foster;Adam C. Krolnik

## August 2004 EDA Nation - Chip Design -

it has historically been those who have pushed the frontiers of integrated circuits. Assertion-based verification and Harry Foster, Azul Systems

<http://www.chipdesignmag.com/edanation/august2004/>

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## Exploiting "architecture for verification" to -

Harry Foster , David Lacey , Adam Krolnik, IEEE-CAS Circuits & Systems analyses can be extended to stochastic mixed-signal systems based on Markov

<http://dl.acm.org/citation.cfm?doid=1629911.1629970>

## Orimitsu Serizawa | LinkedIn -

Experts in Assertion-Based Verification. ASIC (Application Specific Integrated Circuit) Community. ASIC IP Group.

<https://uk.linkedin.com/in/orimitsu>

## INVARIANT SHARING TO SPEED UP FORMAL VERIFICATION -

to correctly design complicated integrated circuits prove the properties based on the legal systems create an inductive assertion,

<http://patents.justia.com/patent/20150213167>

## Implementation of a Transaction Level Assertion -

Assertion based verification (ABV) of RTL models has improved quality assurance of IP blocks and SoC systems to a great extent. Create Account; IEEE Account.

<http://ieeexplore.ieee.org/xpls/icp.jsp?arnumber=4211916>

## Application-specific integrated circuit - -

An application-specific integrated circuit (ASIC) / IP-based design, hard and Design differentiation and customization is achieved by creating custom metal

[http://en.wikipedia.org/wiki/Application-specific\\_integrated\\_circuit](http://en.wikipedia.org/wiki/Application-specific_integrated_circuit)

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2008 ASHRAE Handbook - HVAC: Systems and Equipment (IP Edition) ,  
Creating and Using 3D Models in AutoCAD (Series on Integrated Circuits and  
Systems) ,

<http://sci.math.num-analysis.narkive.com/vhESdjbV/i-have-the-comprehensive-solution-manual-solutions-manual-solutions-manuals-in-electronic-format>

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<http://www.wtrf.com/story/29703075/bert-plug-load-management-system-releases-integration-for-cisco-energy-manager-cem-cloud-solution>

### **1 Evaluating Security Requirements in a -**

we enhance an existing method for creating assertion-based a General-Purpose  
Processor by Combining Assertion Checkers Integrated Circuit

<http://citeseerx.ist.psu.edu/viewdoc/summary?doi=10.1.1.248.3659>

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and such circuits and their transistorized versions were common in computers  
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Adaptive Cooling of integrated Circuits Using Digital Microfluidics

<http://library.xmu.edu.cn/cn/nov/1.xls>

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<http://www.amazon.de/Creating-Assertion-Based-Integrated-Circuits-Systems/dp/1441942181>

## **Creating Assertion- Based IP | Harry D. Foster | -**

Foster, Harry D., Krolnik, Adam C. Creating Assertion-Based IP is an important resource for design and Integrated Circuits and Systems Copyright

<http://www.springer.com/us/book/9780387366418>

## **A SOFTWARE PROGRAMMABLE INFRASTRUCTURE FOR -**

ASSERTION BASED POST SILICON DEBUG integrated circuits. we propose to move the AP to an external IP, called the AP

<http://www.lbd.dcc.ufmg.br/colecoes/sforum/2011/0012.pdf>

## **Eine Methode zur formalen Spezifikation und -**

Eine Methode zur formalen Spezifikation und Umsetzung von Bezeichnungskonventionen f r fachkonzeptionelle Informationsmodelle

[http://www.academia.edu/656328/Eine\\_Methode\\_zur\\_formalen\\_Spezifikation\\_und\\_Umsetzung\\_von\\_Bezeichnungskonventionen\\_f%C3%BCr\\_fachkonzeptionelle\\_Informationsmodelle](http://www.academia.edu/656328/Eine_Methode_zur_formalen_Spezifikation_und_Umsetzung_von_Bezeichnungskonventionen_f%C3%BCr_fachkonzeptionelle_Informationsmodelle)

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## **Instrumenting AMS Assertion Veri cation on -**

Instrumenting AMS Assertion Veri cation on in an integrated circuit consisting of several IP cores, Towards Assertion Based Veri cation of Analog and Mixed

[http://www.eda.org/verilog-ams/htmlpages/public-docs/AMS\\_Assertions/AMS-PAPER.pdf](http://www.eda.org/verilog-ams/htmlpages/public-docs/AMS_Assertions/AMS-PAPER.pdf)

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