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By Harry D. Foster;Adam C. Krolnik

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http://en.wikipedia.org/wiki/Application-specific_integrated_circuit

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to correctly design complicated integrated circuits prove the properties based on the legal systems create an inductive assertion,

<http://patents.justia.com/patent/20150213167>

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CiNii - Series on Integrated Circuits and -

Springer Science+Business Media 2010 Series on Integrated Circuits and Systems : circuits, transceivers and systems. based IP. Harry D. Foster, Adam C. Krolnik.

<http://ci.nii.ac.jp/ncid/BA76437098>

Electrical Engineering - Scribd -

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<http://sci.math.num-analysis.narkive.com/vhESdjbV/i-have-the-comprehensive-solution-manual-solutions-manual-solutions-manuals-in-electronic-format>

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http://oil.carboncapturereport.org/cgi-bin/dailyreport_kml?DATE=2012-04-16&r=462327795.859693&type=2

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Orimitsu Serizawa | LinkedIn -

Experts in Assertion-Based Verification. ASIC (Application Specific Integrated Circuit) Community. ASIC IP Group.

<https://uk.linkedin.com/in/orimitsu>

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Assertion based verification (ABV) of RTL models has improved quality assurance of IP blocks and SoC systems to a great extent. Create Account; IEEE Account.

<http://ieeexplore.ieee.org/xpls/icp.jsp?arnumber=4211916>

Instrumenting AMS Assertion Verification on -

Instrumenting AMS Assertion Verification on in an integrated circuit consisting of several IP cores, Towards Assertion Based Verification of Analog and Mixed

http://www.eda.org/verilog-ams/htmlpages/public-docs/AMS_Assertions/AMS-PAPER.pdf

SOCcentral: Mixed-Signal Design Trends and -

(application specific integrated circuits challenges creating increasingly complex mixed-signal IP and been using assertion-based

<http://soccentral.com/results.asp?EntryID=38597>

1 Evaluating Security Requirements in a -

we enhance an existing method for creating assertion-based a General-Purpose Processor by Combining Assertion Checkers Integrated Circuit

<http://citeseerx.ist.psu.edu/viewdoc/summary?doi=10.1.1.248.3659>

August 2004 EDA Nation - Chip Design -

it has historically been those who have pushed the frontiers of integrated circuits. Assertion-based verification and Harry Foster, Azul Systems

<http://www.chipdesignmag.com/edanation/august2004/>

Flip-flop (electronics) - Wikipedia, the free -

and such circuits and their transistorized versions were common in computers even after the introduction of integrated circuits, circuit based on Create a

[http://en.wikipedia.org/wiki/Latch_\(electronics\)](http://en.wikipedia.org/wiki/Latch_(electronics))

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IEEE Xplore Abstract (Keywords) - Assertion -

Assertion support in high-level synthesis design flow integrated circuit design; RTL verification process; VHDL language; assertion support; assertion-based

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