

Design Recipes For FPGAs: Using Verilog And VHDL (Embedded Technology) By Peter Wilson

By Peter Wilson

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Design Recipes for FPGAs: Using Verilog and VHDL, studying an embedded system which focuses on FPGA design. Peter Wilson is an Academic in the

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Peter Wilson January 09, 2008 Editor's Note: I was recently perusing a new book called Design Recipes for FPGAs that was written by Peter Wilson

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