

FinFET Modeling For IC Simulation And Design: Using The BSIM-CMG Standard By Yogesh Singh Chauhan;Darsen Duane Lu;Vanugopalan Sriramkumar

By Yogesh Singh Chauhan;Darsen Duane Lu;Vanugopalan Sriramkumar

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This book is the first to explain FinFET modeling for IC simulation and the industry standard for FinFET modeling BSIM-CMG. Darsen D Lu; Yogesh Singh Chauhan;

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<http://www.synopsys.com/Tools/Implementation/SignOff/Pages/StarRC-ds.aspx>

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Synopsys and TSMC Collaborate to Deliver 16-nm -

CustomSim and FineSim bring accuracy for latest FinFET modeling Synopsys' Laker statistical modeling, and MOS aging simulation. IC Validator
<http://news.synopsys.com/index.php?item=123511>

BSIM-CMG FinFET Model - Cadence Community -

BSIM-CMG FinFET Model Work has also been ongoing inside of Cadence to ensure that tools support BSIM-CMG FinFET models. Cadence simulation, 3D-IC, and 16nm
http://community.cadence.com/cadence_blogs_8/b/ii/archive/2013/01/21/bsim-cmg-finfet-model-how-academia-and-industry-empowered-the-next-transistor

TSMC Certifies Synopsys Design Tools for 16-nm -

10-nm technology requirements in Synopsys' IC and 3-D FinFET modeling ; delivery of accurate circuit simulation results for the latest FinFET

<http://news.synopsys.com/2015-04-06-TSMC-Certifies-Synopsys-Design-Tools-for-16-nm-FinFET-Plus-Production-and-for-10-nm-Early-Design-Starts>

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<http://www.devriesboeken.nl/boeken/technische-wetenschappen-algemeen/gb/finfet-modeling-f-or-ic-simulation-and-design-niknejad-ai-khandelwal-sourabh-sriramkumar-vanugopalan-duarte-juan-pablo-payvadosi-navid-chauhan-yogesh-singh-lu-darsen-duane-hu-chenming->

Professor ChenMing Hu Introduces His Book: FinFET -

Feb 10, 2015 Professor ChenMing Hu Introduces His Book: FinFET Modeling for IC Simulation and Design.

<http://www.youtube.com/watch?v=aUtzUAoesNI>

BSIM- CMG model parameter extraction - FinFET -

FinFET Modeling for IC Simulation and Design. Using the BSIM-CMG Standard. Edited By Yogesh Singh Chauhan, Darsen D. Lu, BSIM-CMG Parameter Extraction Strategy.

<http://www.sciencedirect.com/science/article/pii/B9780124200319000117>

Darsen D. Lu (Author of Finfet Modeling for IC -

Darsen D. Lu is the author of Finfet Modeling for IC Simulation and Design (0.0 avg rating, 0 ratings, 0 reviews, published 2014)

http://www.goodreads.com/author/show/7182981.Darsen_D_Lu

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Finfet Modeling for IC Simulation and Design : Using the Bsim-Cmg Standard (Yogesh Chauhan) at Booksamillion.com. This book is the first to explain FinFET modeling

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Compact Modeling, FinFETs, Mug-FETs, Yogesh S. Chauhan, Sungho Kim, Darsen Lu, Angada Sachid, Aditya Medury; Title 1 20

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