

High Efficiency Video Coding (HEVC): Algorithms And Architectures (Integrated Circuits And Systems)

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<http://www.youtube.com/watch?v=nUhIJmMvg00>

Reference pictures in HEVC H.265 - VideoHelp -

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<http://forum.videohelp.com/threads/372018-Reference-pictures-in-HEVC-H-265>

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Amazon.it: Collana: Integrated Circuits and Systems; Lingua: Inglese;
ISBN-10: 3319068946;

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Guilherme Correa -

the Group of Architectures and Integrated Circuits on video coding algorithms and digital systems algorithms; High Efficiency Video Coding (HEVC)

<http://guirc.info/>

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What is HEVC? High Efficiency Video Coding, H.265, and 4K compression explained. High Efficiency Video Coding (HEVC), also known as H.265, promises twice the

<http://www.cnet.com/news/what-is-hevc-high-efficiency-video-coding-h-265-and-4k-compression-explained/>

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HEVC, High Efficiency Video Coding, is the new standard for video compression that has the potential to deliver better performance than earlier standards such as H

<https://www.encoding.com/hevc-high-efficiency-video-coding/>

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<https://store.kobobooks.com/en-us/ebook/high-efficiency-video-coding-hevc>

High Efficiency Video Coding (HEVC) - Springer -

High Efficiency Video Coding (HEVC) Algorithms and Architectures. Editors: Vivienne Sze, Madhukar Budagavi, Gary J. Sullivan Integrated Circuits and Systems

<http://link.springer.com/book/10.1007/978-3-319-06895-4>

Call for Papers: Special Issue on Real-Time -

and Systems for High Efficiency Video Coding Energy-Aware Circuits and Systems for HEVC and video processing systems and algorithms

<http://www.sigarch.org/2015/05/04/call-for-papers-special-issue-on-real-time-energy-aware-circuits-and-systems-for-hevc-and-for-its-3d-and-svc-extensions/>

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<http://www.learnr.pro/view/book/56496-high-efficiency-video-coding-hevc-algorithms-and-architectures-integrated-circuits-and-systems>

Download DirectX Video Acceleration Specification -

This document contains the specification for support of the High Efficiency Video Coding (HEVC) codec within the Microsoft Windows DirectX Video Acceleration (DXVA

<http://www.microsoft.com/en-us/download/details.aspx?id=39947>

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<http://www.slideshare.net/rvarun7777/final-seminar-46117193>

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High Efficiency Video Coding (HEVC) resources and reference software. High Efficiency Video Coding (HEVC) Format Range Extension (RExt) Scalability

<http://www.hevc.info/>

Real-time End-to-End H.265/ HEVC Solution for -

3.2 High Performance H.265/HEVC Decoder on Intel Overview of the High Efficiency Video Coding (HEVC) IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS FOR VIDEO

<https://software.intel.com/en-us/android/articles/real-time-end-to-end-h265hevc-solution-for-intel-architecture-based-platforms>

VLSI Circuits, Systems, and Architectures for -

and upcoming H.265/HEVC (high efficiency video coding) algorithms, high VLSI circuits, systems, and architectures are mandatory to

<http://www.hindawi.com/journals/vlsi/si/832927/cfp/>

Programmable Low-Power Multicore Coprocessor -

Programmable Low-Power Multicore Coprocessor Architecture for HEVC/H.265 In-Loop The High Efficiency Video Coding (HEVC) IEEE Circuits and Systems Society

<http://ieeexplore.ieee.org/xpl/articleDetails.jsp?arnumber=6954471>

High Efficiency Video Coding (HEVC), Vivienne -

Fishpond Australia, High Efficiency Video Coding (HEVC): Algorithms and Architectures (Integrated Circuits and Systems) by Madhukar Budagavi (Edited) Vivienne Sze

<http://www.fishpond.com.au/Books/High-Efficiency-Video-Coding-HEVC-Vivienne-Sze-Edited-by-Madhukar-Budagavi-Edited-by/9783319068947>

Parallel algorithms and architectures for low -

Parallel algorithms and architectures for low The next generation standard called High Efficiency Video Coding (HEVC), CMOS digital integrated circuits,

<http://citeseerx.ist.psu.edu/showciting?cid=23387069>

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<http://www.worldcat.org/title/high-efficiency-video-coding-hevc-algorithms-and-architectures/oclc/889942070>

News | Energy- Efficient Multimedia Systems Group -

in IEEE Transactions on Circuits and Systems for Video book High Efficiency Video Coding (HEVC) Algorithm paves path to better video.

<http://www.rle.mit.edu/eems/news/>

High Efficiency Video Coding | MPEG -

Read more about N15139, High Efficiency Video Coding (HEVC) Test Model 16 (HM16) Improved Encoder Description Update 2

<http://mpeg.chiariglione.org/standards/mpeg-h/high-efficiency-video-coding>

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<https://www.deepdyve.com/lp/institute-of-electrical-and-electronics-engineers/efficient-integer-dct-architectures-for-hevc-V5qAp8dlte>

High Efficiency Video Coding - Wikipedia, the -

High Efficiency Video Coding (HEVC) of the box support for HEVC using Ittiam Systems binary arithmetic coding (CABAC) algorithm that is fundamentally

http://en.wikipedia.org/wiki/High_Efficiency_Video_Coding

High Efficiency Video Coding (HEVC): Algorithms -

This book provides developers, engineers, researchers and students with detailed knowledge about the High Efficiency Video Coding (HEVC) standard. HEVC is the

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