

# Mastering The I2C Bus: LabWorX 1 By Vincent Himpe

By Vincent Himpe

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<http://www.eevblog.com/forum/projects/multiple-microcontrollers-communication/>

Aug 20, 2014 Transcript of "Design of dual master i2 c bus controller" 1. Vincent Himpe, Mastering the I2C bus Elektor Verlag publications, 2011. [7].

<http://www.slideshare.net/ijreteditor/design-of-dual-master-i2-c-bus-controller>

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by Vincent Himpe. 2006, Page 1 of 1 < See All Copies 2006 , Elektor Electronics Mastering the I2C Bus. by Vincent Himpe.

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I'm not familiar with the I2C bus structure and we're in a process establishing both new uP, I just read the book "Mastering the I2C Bus" by Vincent Himpe,

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Sep 08, 2014 Common I2C bus speeds are 100 Kbit/s standard mode and 10Kbit/s low speed mode, Vicent Himpe, Mastering the I2C Bus: LabWorX 1 . [3]

<http://www.slideshare.net/ijeraeditor/interfacing-of-pic-18f252-microcontroller-with-real-time-clock-via-i2c-protocol>

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