

# Processor Design: System-On-Chip Computing For ASICs And FPGAs

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such as microprocessor cores that form a system-on-chip. more complexity in the computer-aided design (CAD) system, Field-programmable gate array (FPGA

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IEEE Design & Test of 98 A1 - Scott Davidson PY - 2008/01/31 KW - processor architecture  
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Commercial and open projects for processor design on FPGA. Processor Design: System-On-Chip Computing for ASICs and FPGAs, Springer 2007 . 3) D. Liu,

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a move that ultimately made the single-chip CPU final design a in the general computing chip) processors in a multiprocessor system,

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