

VLSI High-Speed I/O Circuits By Hongjiang Song

By Hongjiang Song

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as clock and data recovery circuits for high speed serial I/O and frequency of the charge pump PLL, Song, Hongjiang (Committee

<http://repository.asu.edu/items/14500>

Hongjiang Song: Arizona State University, USA USA) where he has been offering courses in VLSI Analog Signal Processing, VLSI High-Speed I/O Circuits,

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<https://www.coursehero.com/file/p1f4ek/VLSI-High-Speed-IO-Circuits-Theoretical-Basis-Circuit-Architectures-Behavioral/>

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EEE598 ST: Serial Links. Instructor: Dr. Hongjiang Song hongjiang.song@asu.edu. Credits: 3 hours.

Prerequisites: Basic VLSI analog circuits. Basic VLSI digital circuits

http://engineering.asu.edu/sites/default/files/shared/syllabi/EEE_598_SL_Fa12.doc

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<http://yadda.icm.edu.pl/yadda/element/bwmeta1.element.ieee-000005784639>

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Hongjiang Song. Hongjiang.Song@asu.edu. where he has been offering graduate level courses in VLSI analog signal processing circuits and VLSI high-speed I/O circuits.

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<http://www.ece.ncsu.edu/asic/ece733/papers/drivers/CMLprogramTermination.pdf>

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ECE554 Advanced Analog VLSI Circuit Design. switched capacitor circuits, high speed I/O, and PLL design. Additional Topics will be added based on student interest.

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Dr. Hongjiang Song currently serves as adjunct professor as Arizona State University where he has been offering courses in VLSI Analog Signal Processing and VLSI High
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