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By Hongjiang Song

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Mixed-Signal VLSI Circuits for Particle Detector Instrumentation Testing CMOS parts is becoming more difficult due to the proliferation of high-speed I/O circuits

<http://citeseerx.ist.psu.edu/showciting?cid=1625504>

A Bang-Bang All-Digital PLL for Frequency -

as clock and data recovery circuits for high speed serial I/O and frequency of the charge pump PLL, Song, Hongjiang (Committee

<http://repository.asu.edu/items/14500>

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<https://www.linkedin.com/pub/hongjiang-li/5a/b62/689>

Jitter transfer function model and VLSI jitter -

in VLSI high-speed I/O circuits. 23rd IEEE International SOC Conference. Opis fizyczny. Daty. utworzono. 2011-06-03. Tw rcy. autor Hongjiang Song. Intel

<http://yadda.icm.edu.pl/yadda/element/bwmeta1.element.ieee-000005784639>

Programmable termination for CML I/O's in high -

Programmable termination for CML I/O's in high speed CMOS transceivers - VLSI Circuits Digest of Technical Papers, 2002. Symposium on Created Date:

<http://www.ece.ncsu.edu/asic/ece733/papers/drivers/CMLprogramTermination.pdf>

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Hongjiang Song. Hongjiang.Song@asu.edu. where he has been offering graduate level courses in VLSI analog signal processing circuits and VLSI high-speed I/O circuits.

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EEE598 ST: Serial Links. Instructor: Dr. Hongjiang Song hongjiang.song@asu.edu. Credits: 3 hours.

Prerequisites: Basic VLSI analog circuits. Basic VLSI digital circuits

http://engineering.asu.edu/sites/default/files/shared/syllabi/EEE_598_SL_Fa12.doc

Design of high- speed clock and data recovery -

This article describes the various architectures for a high-speed clock and data high-speed I/O circuit on VLSI Circuits Digest of

<http://link.springer.com/article/10.1007/s10470-007-9093-1>

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<https://www.linkedin.com/pub/tarun-arora/3a/510/957>

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<http://www.openisbn.com/isbn/1441559876/>

VLSI Technology - Wikipedia, the free -

VLSI Technology, Inc was a company VLSI entered the merchant high speed static RAM VLSI developed a range of algorithms and circuits for the GSM standard and

https://en.m.wikipedia.org/wiki/VLSI_Technology

EEE598D, Analog Signal Processing & Filter -

Dr. Hongjiang Song, Arizona State University EEE598D: Analog Filter & Signal Processing Circuits Instructor: High Speed Common Mode Rejection.

http://www.oocities.org/fudinggefilter/Note9_sc2.pdf

ECE554 Advanced Analog VLSI Circuit Design - -

ECE554 Advanced Analog VLSI Circuit Design. switched capacitor circuits, high speed I/O, and PLL design. Additional Topics will be added based on student interest.

<http://apsellab.ece.cornell.edu/ece554/sp08/>

Vlsi high- speed i/o circuits theoretical basis, -

VLSI High-Speed I/O Circuits Theoretical EEE598 VLSI High-Speed I/O Circuits Fall 2010 Speed I/O Circuits Overview c Dr. Hongjiang Song,

<https://www.coursehero.com/file/p1f4ek/VLSI-High-Speed-IO-Circuits-Theoretical-Basis-Circuit-Architectures-Behavioral/>

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This text covers various topics related to the analysis, design, and implementation techniques of VLSI analog filter and signal processing circuits, including the

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